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APPLICANT: Drew Guckenberger, et al.

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUB-CLASS	FILING DATE IF APPROPRIATE
N	5,581,212	12/03/96	Huang et al.	330/253	
AK	6,218,905 B1	04/17/01	Sanders et al.	330/308	
AK	6,359,517 B1	03/19/02	Colaco	330/308	
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A	6,529,078	03/04/03	Gosser et al.	330/255	

OTHER DOCUMENTS
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M	H.-M. Rein et al. "Design Considerations for Very-High-Speed Si-Bipolar IC's Operating up to 50Gb/s", <i>IEEE Journal of Solid-State Circuits</i> , vol.31, No.8, pp. 1076-1090, August 1996.
KA	S.S. Mohan et al. "A 2.125 Gbaud 1.6kΩ Transimpedance Preamplifier in 0.5μm CMOS", <i>IEEE Custom Integrated Circuits Conference 1999</i> , pp. 513-516, 1999.
AK	A. Schild, et al., "Amplifier Array for 12 Parallel 10Gb/s Optical-Fiber Links Fabricated in a SiGe Production Technology", <i>2002 IEEE RFIC Symposium</i> , pp. 89-92, 2002.
AK	D. Guckenberger, et al. "Novel low-voltage, low-power Gb/s transimpedance amplifier architecture." Proc. SPIE, Conf. on VLSI Circuits and Systems, Canary Islands, Spain, May 19-21, 2003, pp. 274-285.
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